L Number	Hits	Search Text	DB	Time stamp
31	1	6003038.pn.	USPAT;	2004/02/23 07:23
-	1	5479621.pn.	USPAT;	2004/02/20 13:50
	1	EP-279953-\$.pn.	EPO USPAT; EPO	2004/02/11 13:56
-	3	emulation adj instruction	IBM_TDB	2004/02/11 14:01
-	1	"real-time cisc architecture hw emulator"	IBM_TDB	2004/02/11 14:02
-	1	5752073.pn.	USPAT; EPO	2004/02/11 14:03
-	13	(("8-bit" or ("8" adj bit)) and (memory or processor or microprocessor)).ti.	USPAT; EPO	2004/02/12 08:17
-	1514	(("8-bit" or ("8" adj bit)) adj (processor or microprocessor))	USPAT; EPO	2004/02/12 08:16
-	77	712/242.ccls.	USPAT; EPO	2004/02/12 08:17
-	71	712/243.ccls.	USPAT; EPO	2004/02/12 08:17
-	21	(712/242.ccls. or 712/243.ccls.) and ("8-bit" or (((("8-bit" or ("8" adj bit)) near2 (processor or microprocessor))) adj bit))	USPAT; EPO	2004/02/12 08:18
-	11	(712/242.ccls. or 712/243.ccls.) and (("8-bit" or (((("8-bit" or ("8" adj bit)) near2 (processor or microprocessor))) adj bit)) with instruction)	USPAT; EPO	2004/02/12 09:24
-	0	(712/242.ccls. or 712/243.ccls.) and bytecode	USPAT; EPO	2004/02/12 09:24
-	719	bytecode	USPAT; EPO	2004/02/12 09:24
-	49	bytecode and 712.clas.	USPAT; EPO	2004/02/12 09:27
-	153	bytecode with \$4processor	USPAT; EPO	2004/02/12 10:41
-	67	(bytecode with \$4processor) and \$3routine	USPAT; EPO	2004/02/12 10:43
-	239	(((user adj defined) or predefined) adj instruction)	USPAT; EPO	2004/02/12 10:44
-	202	(((user adj defined) or predefined) adj instruction) and (processor or microprocessor)	USPAT; EPO	2004/02/12 10:44
-	80	(((user adj defined) or predefined) adj instruction) and (processor or microprocessor) and (bytecode or "8-bit" or "8 bit")	USPAT; EPO	2004/02/12 10:45
-	31	(((user adj defined) or predefined) adj instruction) and (processor or microprocessor) and (bytecode or "8-bit" or "8 bit") and subroutine	USPAT; EPO	2004/02/12 10:47
-	1	((((user adj defined) or ("user-defined")) with predefined) with instruction) and (processor or microprocessor) and	USPAT; EPO	2004/02/12 10:49
-	68	(bytecode or "8-bit" or "8 bit") and subroutine (((user adj defined) or ("user-defined")) with instruction) and (processor or microprocessor) and (bytecode or "8-bit"	USPAT; EPO	2004/02/12 10:49
-	1	or "8 bit") and subroutine (((user adj defined) or ("user-defined")) with instruction with subroutine) and (processor or microprocessor) and (bytecode or "8-bit" or "8 bit")	USPAT; EPO	2004/02/12 10:50
-	11	(macroinstruction with subroutine) and (processor or microprocessor) and (bytecode or "8-bit" or "8 bit")	USPAT; EPO	2004/02/12 10:50
-	1	6003038.pn.	USPAT; EPO	2004/02/17 10:59
-	14	("5446902" "5511196" "5615362" "5632034" "5682532" "5692193" "5706506" "5736984" "5740441" "5745758" "5787431" "5797007" "5815718" "5848423").PN.	USPAT	2004/02/17 10:57

FPO		200	T-12 (200 I-	LICDAT	2004/02/47 44.02
- 106 717/118.ccls. USPAT; EPO USPAT; 2004/02/17 11:03 EPO USPAT; 2004/02/17 11:03 EPO USPAT; 2004/02/17 11:04 EPO USPAT; 2004/02/17 11:16 EPO USPAT; 2004/02/17 11:16 EPO USPAT; 2004/02/17 11:17 EPO USPAT; 2004/02/17 11:17 EPO USPAT; 2004/02/17 11:18 EPO USPAT; 2004/02/19 16:37 EPO USPAT; 2004/02/19 17:12 EPO USPAT; 2004/02/20 13:59 EPO USPAT; 2004/02/20 13:59 EPO USPAT; 2004/02/20 14:10 EPO USPAT; 2004/02/20 14:10 EPO USPAT; 2004/02/20 14:10 EPO USPAT; 2004/02/20 14:11 EPO	-	208	712/209.ccls.	USPAT;	2004/02/17 11:02
- 88 717/118.ccls. and processor		100	747/440		2004/02/17 11:02
- 88 717/118.ccls. and processor - 83 717/118.ccls. and processor and memory - 83 717/118.ccls. and processor and memory - 1717/118.ccls. and processor and memory and address and fetch\$3 - 1717/118.ccls. and processor and memory and address and fetch\$3 - 1717/118.ccls. and processor and memory and address and fetch\$3 - 1717/118.ccls. and processor and memory and address and fetch\$3 - 1717/118.ccls. and processor and memory and address and fetch\$3 - 1717/118.ccls. and processor and memory and address and fetch\$3 - 1717/118.ccls. and processor and memory and address and fetch\$3 - 1717/118.ccls. and processor and memory and address and fetch\$3 - 1717/118.ccls. and processor and memory and address and fetch\$3 - 1717/118.ccls. and processor and memory and address and fetch\$3 - 1717/118.ccls. and processor and memory and address and fetch\$3 - 1717/118.ccls. and processor and memory and address and fetch\$3 - 1717/118.ccls. and processor and memory and address and fetch - 1717/118.ccls. and processor and memory and address and fetch - 1717/118.ccls. and processor and memory and address and fetch - 1717/118.ccls. and processor and memory and address and fetch - 1718/118.ccls. and processor and memory and address and fetch - 1718/118.ccls. and processor and memory and address and fetch - 1718/118.ccls. and processor and memory and address and fetch - 1718/118.ccls. and processor and memory and address and fetch - 1718/118.ccls. and processor and memory and address and fetch - 1718/118.ccls. and processor and memory and address and fetch - 1718/118.ccls. and processor and memory and address and fetch - 1718/118.ccls. and processor and memory and address and fetch - 1718/118.ccls. and processor and memory and address and fetch - 1719/118.ccls. and processor and memory infecches and good/02/17111:16 - 1719/118.ccls. and processor and memory infecches and good/02/17	-	106	/1//118.CCIS.		2004/02/17 11:03
Say			747/4401	1	2004/02/17 11:02
- 83 717/118.ccls. and processor and memory	-	88	/1//118.ccis. and processor	1	2004/02/17 11:03
S1			747/440 cele and average and manage	1	2004/02/17 11:04
- 51 717/118.ccls. and processor and memory and address	-	83	/1//118.ccis. and processor and memory		2004/02/17 11:04
- 13 717/118.ccls. and processor and memory and address and fetch\$3 bytecode adj execut\$3 USPAT; EPO USPAT; EP			717/110 ade and averages and moment and address	1	2004/02/17 11:04
- 13 717/118.ccls. and processor and memory and address and fetch\$3 - 146 bytecode adj execut\$3 - 0 (bytecode adj execut\$3) with (microcode or microinstruction) - 0 (bytecode adj execut\$3) with (subroutine) - 0 (bytecode adj execut\$3) with (subroutine) - 1 6349377.pn 1 6349377.pn 210 routine near4 (data adj memory) - 210 routine near4 (data adj memory) - 210 routine near4 (data adj memory) - 2276 shift\$3 near address - 298 (shift\$3 near address) with generat\$3 - 298 (shift\$3 near address) with generat\$3 - 107 (shift\$3 with address) and bytecode - 12 (shift\$3 with address) same bytecode same ("program counter" or "instruction register") - 1 (shift\$3 with address) same bytecode same ("program counter" or "instruction register") - 1 ("8-bit" or "8 bit") with ("instruction memory") - 12 (Shift\$3 with address) same bytecode same ("program counter" or "instruction register") - 105 ("8-bit" or "8 bit") with ("instruction memory") - 107 (SPAT; 2004/02/20 14:13	-	21	/1//118.ccis. and processor and memory and address	1	2004/02/17 11:04
Fetch\$3		١	747/440 and and any access and memory and address and	1	2004/02/17 11:16
- 146 bytecode adj execut\$3 - 0 (bytecode adj execut\$3) with (microcode or microinstruction) - 0 (bytecode adj execut\$3) with (subroutine) - 1 (6349377.pn.	-	13	l '		2004/02/17 11:16
- 0 (bytecode adj execut\$3) with (microcode or microinstruction)		146		1	2004/02/17 11:17
- 0 (bytecode adj execut\$3) with (microcode or microinstruction) (bytecode adj execut\$3) with (subroutine) USPAT; EPO USPAT; 2004/02/17 11:18 EPO USPAT; 2004/02/17 11:18 EPO USPAT; 2004/02/17 11:18 EPO USPAT; 2004/02/19 16:37 EPO USPAT; 2004/02/19 17:12 EPO USPAT; 2004/02/20 13:53 EPO USPAT; 2004/02/20 13:59 EPO USPAT; 2004/02/20 13:59 EPO USPAT; 2004/02/20 13:59 EPO USPAT; 2004/02/20 14:00 EPO USPAT; 2004/02/20 14:10 EPO USPAT; 2004/02/20 14:10 EPO USPAT; 2004/02/20 14:10 EPO USPAT; 2004/02/20 14:11 counter" or "instruction register") USPAT; 2004/02/20 14:11 counter" or "instruction register") USPAT; 2004/02/20 14:11 SPO USPAT; 2004/02/20 14:11 SPO USPAT; 2004/02/20 14:11 SPO USPAT; 2004/02/20 14:11 COUNTER" or "B bit") with ("instruction memory") USPAT; 2004/02/20 14:13	-	146	bytecode adj executs3		2004/02/17 11:17
microinstruction		ا ا	(h. tagada adi ayan tt2) with (migragada ar		2004/02/17 11:19
- 0 (bytecode adj execut\$3) with (subroutine) USPAT; EPO USPAT; EP	-	"			2004/02/17 11:16
- 1 6349377.pn.		١ ,			2004/02/17 11:19
- 1 6349377.pn. USPAT; EPO EPO USPAT; EPO US	-	"	(bytecode adj execut§3) widi (subroutille)		2004/02/17 11:16
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- 210 routine near4 (data adj memory) - 210 routine near4 (data adj memory) - 42 routine near4 store\$ near4 (data adj memory) - 42 routine near4 store\$ near4 (data adj memory) - 2276 shift\$3 near address - 298 (shift\$3 near address) with generat\$3 - 298 (shift\$3 near address) with generat\$3 - 107 (shift\$3 near address) near3 generat\$3 - 12 (shift\$3 with address) and bytecode - 12 (shift\$3 with address) same bytecode same ("program counter" or "instruction register") - 46 ("8-bit" or "8 bit") with ("instruction memory") - 100 USPAT; 2004/02/20 14:10 - 2004/02/20 14:11 - 2004/02/20 14:11 - 2004/02/20 14:11 - 2004/02/20 14:11 - 2004/02/20 14:11	-	1	0349377.pii.	1	2004/02/19 10.37
- 210 routine near4 (data adj memory) - 42 routine near4 store\$ near4 (data adj memory) - 2276 shift\$3 near address - 298 (shift\$3 near address) with generat\$3 - 107 (shift\$3 near address) near3 generat\$3 - 12 (shift\$3 with address) and bytecode - 12 (shift\$3 with address) same bytecode same ("program counter" or "instruction register") - 46 ("8-bit" or "8 bit") with ("instruction memory") - 2004/02/19 17:12 - 2004/02/19 17:12 - 2004/02/20 13:53 - 2004/02/20 13:59 - 2004/02/20 14:00 - 2004/02/20 14:10 - 2004/02/20 14:11 - EPO - 2004/02/20 14:11 - EPO - 2004/02/20 14:11		72	cubrouting poar4 (data adi memony)	1	2004/02/19 17:12
- 210 routine near4 (data adj memory) - 42 routine near4 store\$ near4 (data adj memory) - 2276 shift\$3 near address - 298 (shift\$3 near address) with generat\$3 - 107 (shift\$3 near address) near3 generat\$3 - 12 (shift\$3 with address) and bytecode - 12 (shift\$3 with address) same bytecode same ("program counter" or "instruction register") - 46 ("8-bit" or "8 bit") with ("instruction memory") - 2004/02/19 17:12 EPO USPAT;	-	/2	Subroutine near4 (data adj memory)		2004/02/19 17.12
- 42 routine near4 store\$ near4 (data adj memory) - 2276 shift\$3 near address - 298 (shift\$3 near address) with generat\$3 - 107 (shift\$3 near address) near3 generat\$3 - 12 (shift\$3 with address) and bytecode - 12 (shift\$3 with address) same bytecode same ("program counter" or "instruction register") - 46 ("8-bit" or "8 bit") with ("instruction memory") - 2004/02/21 11:33		210	routing pears (data adi memory)		2004/02/19 17:12
- 42 routine near4 store\$ near4 (data adj memory) - 2276 shift\$3 near address - 298 (shift\$3 near address) with generat\$3 - 107 (shift\$3 near address) near3 generat\$3 - 107 (shift\$3 with address) and bytecode - 12 (shift\$3 with address) same bytecode same ("program counter" or "instruction register") - 46 ("8-bit" or "8 bit") with ("instruction memory") USPAT; 2004/02/20 13:59 EPO USPAT; 2004/02/20 14:00 EPO USPAT; 2004/02/20 14:10 EPO USPAT; 2004/02/20 14:11 EPO USPAT; 2004/02/20 14:11	_	210	Toutine near4 (data auj memory)		2004/02/13 17.12
- 2276 shift\$3 near address		42	routine near4 stores near4 (data adi memon)	1	2004/02/19 17:12
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- 107 (shift\$3 near address) near3 generat\$3	_	208	(shift\$3 near address) with generat\$3		2004/02/20 13:59
- 107 (shift\$3 near address) near3 generat\$3 USPAT; EPO USPAT; Counter" or "instruction register") EPO USPAT; EPO USPAT; Counter" or "sbit") with ("instruction memory") USPAT; 2004/02/22 11:33		290	(Sility) fical address) mai generacy	1	200 1/02/20 13.33
- 12 (shift\$3 with address) and bytecode USPAT; 2004/02/20 14:10 EPO - 1 (shift\$3 with address) same bytecode same ("program Counter" or "instruction register") EPO USPAT; 2004/02/20 14:11 EPO USPAT; 2004/02/22 11:33	_	107	(shift\$3 near address) near3 generat\$3		2004/02/20 14:00
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			() = 1.2 3. 5 = 1.2 7 () ()	EPO	,